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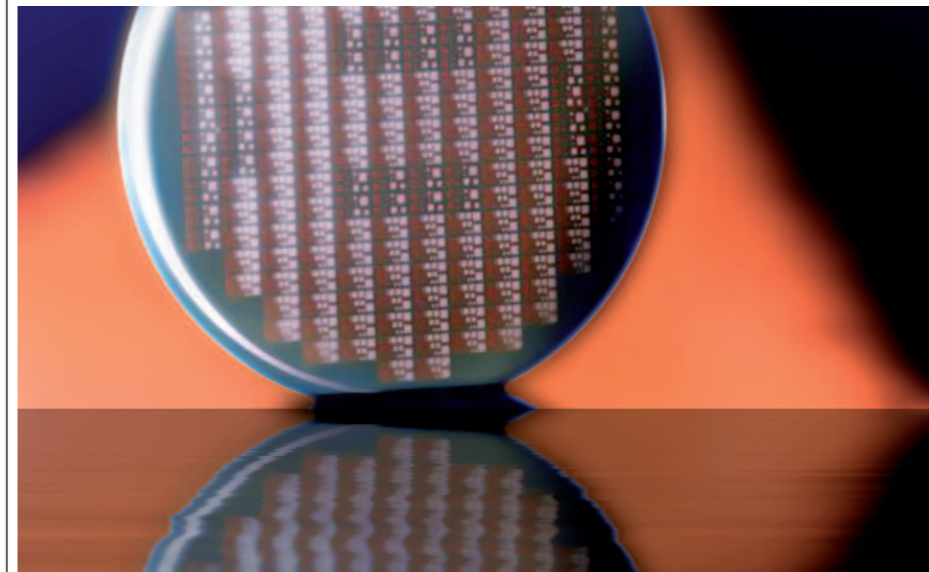
**Fax reply**  
**+49 (0) 351 2583-314**

by 10 | 10 | 2007

- Welcome Party, 15.10.2007
- Dinner, 16.10.2007

(Workshop limited to maximum  
60 participants)

.....  
date / signature



Freestate of Saxony  
Saxony State Ministry  
for Economic Affairs  
and Labour



United Kingdom  
of Great Britain  
and Northern Ireland



Fraunhofer  
Institute  
Material and  
Beam Technology

**Invitation**

16 | 10 | 2007

RAMADA Hotel Dresden  
Wilhelm-Franke-Str. 90  
01219 Dresden

## CONTACT

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CC "Ultrathin functional films"

# German-British Workshop Technologies and Materials for Nanoelectronics

**German-British Workshop  
Technologies and Materials for Nanoelectronics**

Tuesday 16th October 2007  
Moderator: Matthias Werner, NMTC

- 08:40 Registration
- 09:00 Welcome Statement
- Technologies, Analytics:**
- 09:10 **Main Trends and Development Issues for Chip Technologies for sub 50nm Generations**  
Karl Heinz Kuesters, QIMONDA Dresden GmbH & Co. OHG
- 09:35 **A Versatile Tool for Manufacture of Micro-Nano Systems: in-situ Surface Activation, Alignment and Bonding of Wafers, plus Aligned Embossing / Nanoimprint**  
Tony Rogers, Applied Microengineering Ltd (AML)

- 10:00 **Coating Technology for Glass, Flat Panel Displays and other Applications**  
Johannes Strümpfel, von Ardenne Anlagentechnik
- 10:25 Coffee
- 10:50 **Deposition of Low Temperature, Low Stress Thin Films using High Density Remote Plasma Process**  
James Dutson, PlasmaQuest Ltd
- 11:15 **High Resolution Spatial Light Modulators for Semiconductor Mask Writing Down to 45nm Node**  
Michael Wagner, Fraunhofer Institute for Photonic Microsystems
- 11:40 **Characterisation of Materials for Nano-Scale Electronics**  
Alison Crossley, Oxford University Begbroke Science Park
- 12:05 **Summing-up**  
Matthias Werner, NMTC

**PROGRAMME**

[WWW.NANOTECHNOLOGY.DE](http://WWW.NANOTECHNOLOGY.DE)

**Materials, Metrology:**

- 12:10 **High Resolution Analysis Techniques for sub-50nm Field Effect Transistors**  
Michael Hecker, AMD Advanced Micro Devices
- 12:35 **Thin Film Energy Dispersive Spectroscopy for Nanoelectronics**  
Sarah Sharp, Oxford Instruments Analytical
- 13:00 Buffet Lunch, opportunity to visit exhibitors' stands
- 14:15 **Modelling and Simulation for Nanoelectronics Interconnect Systems**  
Reinhard Streiter, Fraunhofer Institute for Reliability and Microintegration
- 14:40 **CCFT - New Processing Technology for the Deposition of Functional Coatings**  
Tony McKie, Memstar Technology
- 15:05 **Laser-Arc Process for Formation of Nano-Structured Carbon Films and Nano-Particles**  
Hans-Joachim Scheibe, Fraunhofer Institute for Material and Beam Technology
- 15:30 Coffee, further opportunity to visit exhibitors' stands
- 16:30 **Formulated Nanomaterials on an Industrial Scale using Hydrothermal Synthesis**  
Ed Lester, University of Nottingham
- 16:55 **Packaging for Microelectronics**  
Joachim Kloeser, AEMtec GmbH
- 17:20 Summary / Round-up
- 17:50 End
- Evening Programme  
Dinner hosted by the British Embassy

**PROGRAMME**

